

Notice of References Cited	Application/Control No. 10/711,838	Applicant(s)/Patent Under Reexamination CHOU, PEI-YU	
	Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, Silicon Processing for the VLSI Era, 2002, Lattice Press, Vol. 4, p. 249.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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